

PACS Newsletter

Nr. 5, 2 October 2006



Current Schedule

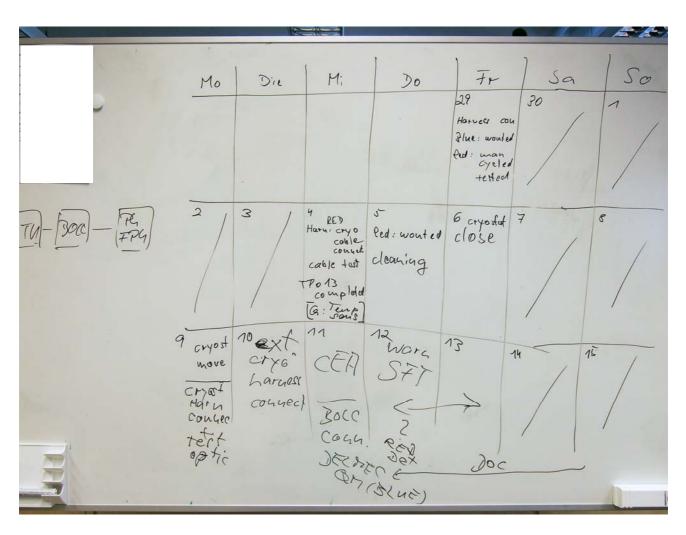


Fig. 1: Schedule as of Friday, 29 September.

The integration of the FPU is nearing its completion. Last activities like mounting of detector units and cryo harnesses, and moving from the clean room to the lab will take us into October 10. The CEA LTU tests are currently foreseen for Wednesday, October 11. SFT Warm would be performed on Thursday, October 12. Friday, the 13th, is margin. The subsequent schedule still depends on the still uncertain DECEMC delivery date.



Fig. 2: One of the culprits for the slight delay in the integration of the FPU: the ESD protection circuit and diode connectors.

The **naming convention** for the PACS mascot is still open. Suggestions so far were:

Paxi Bear Lars Otto

Come on folks, that is nice but I believe you can do better!



The PACS Picture of the Week



Fig. 3: Gerd Jakob, once again fighting the perfidy of the PACS Diode Connectors (German: Diodenstecker). The PACS Thought-Police caught him while thinking an extra "I" in front (credit: AlPog)

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